

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No.: 10/784,112

Filed: February 20, 2004

For: Single Level MIP Filtering Algorithm for Anisotropic Texturing

Confirmation No.: 5254

Commissioner for Patents
Washington, D.C. 20231

POWER OF ATTORNEY BY ASSIGNEE OF ENTIRE INTEREST
(REVOCATION OF PRIOR POWERS)

As assignee of record of each of the patent applications listed in the table of attachment A,

REVOCATION OF PRIOR POWERS OF ATTORNEY

all powers of attorney previously given in each of the listed patent applications are hereby revoked, and

NEW POWER OF ATTORNEY

the following attorneys/agents are hereby appointed to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith: I hereby appoint all attorneys of Thomas, Kayden, Horstemeyer & Risley, LLP, who are listed under the USPTO Customer Number shown below as the attorneys to prosecute this application and to transact all business in the United States Patent and Trademark Office connected therewith, recognizing that the specific attorneys listed under that Customer Number may be changed from time to time at the sole discretion of Thomas, Kayden, Horstemeyer & Risley, LLP, and request that all correspondence about the application be addressed to the address filed under the same USPTO Customer Number.

24504

Patent Trademark Office

Please direct all future correspondence and telephone calls to:

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ASSIGNEE OF ENTIRE INTEREST

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ASSIGNEE CERTIFICATION

The certification under 37 C.F.R. §3.73(b) establishing the right of assignee to take action is attached hereto along with documentation evidencing same. Further, in my official position with VIA Technologies, Inc., I am authorized to sign documents and otherwise act on its behalf in connection with the management and handling of patent applications and other intellectual property matters.

Also, certain applications listed in the attached Table are formally assigned to S3G Co., Ltd. in the accompanying. S3G Co., Ltd. was purchased by VIA Technologies, Inc. and therefore in my official position with VIA Technologies, Inc., I am authorized to sign this document on behalf of those patent applications.

Date: Nov. 17, 2006

Michael Shiuan, VP, Engineering

Name: Michael Shiuan
Title

Attachment A

No.	Serial No	Application Title	Filing Date	Assignment (Reel/Frame)
1	11/382,682	Apparatus and method for alpha blending of digital images	5/10/2006	No data recorded in the PTO
2	10/740,948	Apparatus and Method for High Speed Data Transfer	12/18/2003	014603/0041
3	10/631,336	Method and Circuit for Command Integrity Checking(CIC) in a Graphics Controller	7/30/2003	014204/0713
4	10/666,083	Synchronous Periodical Orthogonal Data Converter	09/19/2006	014530/0235
5	10/784,113	Approximation of Level of Detail Calculation in cubic Mapping without Attribute Delta Function	2/20/2004	014813/0906
6	10/784,112	Single Level MIP Filtering Algorithm for Anisotropic Texturing	2/20/2004	014813/0909
7	10/671,849	Anti-aliasing Line Pixel Coverage Calculation Using Programmable Shader	9/25/2003	014813/0857
8	10/943,488	Partial GuardBand Clipping	9/17/2004	015499/0314
9	10/407,573	System and Method for High Speed Handshaking	4/4/2003	013937/0406
10	11/457,102	HEAD/DATA Request in 3D Graphics	7/12/2006	No data recorded in the PTO
11	10/436,712	Bounding Box in 3D Graphics	5/13/2003	014064/0937